

PRODUCT SPECIFICATION

C u s t o m e r : _____

Product Name : Logic Gate

Part Number : SN74AHCT1G32

I s s u e D a t e : _____

Prepared	Checked	Customer Check
ChenTT	Zelig	

SN74AHCT1G32: Single 2-Input Positive-OR Gate

DESCRIPTIONS

The SN74AHCT1G32 single 2-input positive-OR gate is designed for 2.0V to 5.5V V_{CC} operation.

The SN74AHCT1G32 device performs the Boolean function $Y=A+B$ or $Y=\overline{A} \cdot \overline{B}$ in positive logic. The device is fully specified for partial-power-down applications using I_{off} . The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The SN74AHCT1G32 is available in Green SOT23-5 packages. It operates over an ambient temperature range of -40°C to 125°C.

FEATURES

- **Operating Voltage Range: 2.0V to 5.5V**
- **Low Power Consumption: 0.1µA (TYP)**
- **Operating Temperature Range: -40°C to 125°C**
- **TTL Input are Compatible**
- **Input Accept Voltage to 5.5V**
- **Output Drive: ±32mA at $V_{CC}=5.0V$**
- **I_{off} Supports Partial-Power-Down Mode Operation**
- **Micro SIZE PACKAGES: SOT23-5**

APPLICATIONS

- **AV Receiver**
- **Blu-ray Player and Home Theater**
- **Digital Picture Frame (DPF)**
- **High-Speed Data Acquisition and Generation**
- **Personal Navigation Device (GPS)**
- **Portable Media Player**

PACKAGE/ORDERING INFORMATION ⁽¹⁾

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ⁽²⁾	MSL ⁽³⁾	PACKAGE OPTION
SN74AHCT1G32	SN74AHCT1G32DBVR	-40°C ~125°C	SOT23-5	B32S	MSL3	Tape and Reel,3000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

LOGIC SYMBOL



FUNCTION TABLE

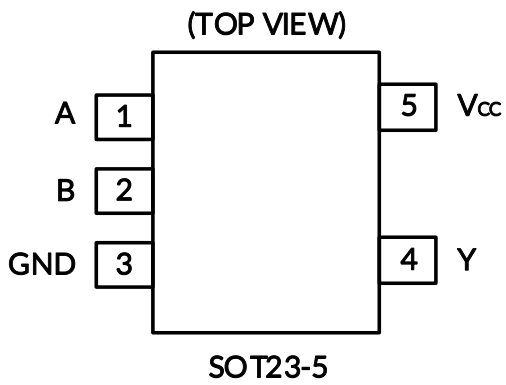
INPUTS		OUTPUT
A	B	Y
H	H	H
L	H	H
H	L	H
L	L	L

$Y=A+B$

H=High Voltage Level

L=Low Voltage Level

PIN CONFIGURATION AND FUNCTIONS (TOP VIEW)



PIN	NAME	I/O	DESCRIPTION
1	A	I	Input
2	B	I	Input
3	GND	G	Ground
4	Y	O	Output
5	V _{CC}	P	Power Pin

REVISION HISTORY

Note: Page numbers for previous revisions may differ from page numbers in the current version.

VERSION	Change Date	Change Item
Rev A.1	2026/01/09	Initial version completed

Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) ⁽¹⁾⁽²⁾

		MIN	MAX	UNIT
V _{CC}	Supply voltage range	-0.5	6.5	V
V _I	Input voltage range ⁽²⁾	-0.5	6.5	V
V _O	Voltage range applied to any output in the high-impedance or power-off state ⁽²⁾	-0.5	6.5	V
V _O	Voltage range applied to any output in the high or low state ⁽²⁾⁽³⁾	-0.5	V _{CC} +0.5	V
I _{IK}	Input clamp current	V _I <0		-50 mA
I _{OK}	Output clamp current	V _O <0		-50 mA
I _O	Continuous output current			±50 mA
Continuous current through V _{CC} or GND			±100	mA

Package Thermal Impedance

PARAMETER	MIN	MAX	UNIT
θ _{JA} Package thermal impedance ⁽⁴⁾ SOT23-5		230	°C/W

Temperature

PARAMETER	MIN	MAX	UNIT
Temperature Junction temperature, T _J ⁽⁵⁾	-65	150	°C
Storage temperature, T _{stg}	-65	150	

ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

	VALUE	UNIT	 ESD SENSITIVITY CAUTION ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.
Human-Body Model (HBM)	±2000	V	
Charged-Device Model (CDM)	±1000		
Machine Model (MM)	±200		

NOTE:

- (1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of V_{CC} is provided in the Recommended Operating Conditions table.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of T_{J(MAX)}, R_{θJA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{θJA}. All numbers apply for packages soldered directly onto a PCB.

ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range (TYP values are at $T_A = +25^\circ\text{C}$, Full= -40°C to 125°C , unless otherwise noted.) ⁽¹⁾

Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
Supply voltage	V_{CC}	Operating	2.0	5.5	V
High-level input voltage	V_{IH}	$V_{CC}=2.0\text{V}$	1.0		
		$V_{CC}=3.3\text{V}$	1.5		V
		$V_{CC}=4.5\text{V to } 5.5\text{V}$	2.0		
Low-level input voltage	V_{IL}	$V_{CC}=2.0\text{V}$		0.3	
		$V_{CC}=3.3\text{V}$		0.55	V
		$V_{CC}=4.5\text{V to } 5.5\text{V}$		0.8	
Input voltage	V_I		0	5.5	V
Output voltage	V_O		0	V_{CC}	V
Input transition rise or fall	$\Delta t/\Delta v$	$V_{CC}=2.0\text{V to } 5.5\text{V}$		5	ns/V
Operating temperature	T_A		-40	125	$^\circ\text{C}$

(1) All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

DC Characteristics

PARAMETER	TEST CONDITIONS	V_{CC}	TEMP	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT
V_{OH}	$I_{OH} = -100\mu\text{A}$	2.0V to 5.5V		$V_{CC}-0.1$			
	$I_{OH} = -8\text{mA}$	2.0		1.6			
	$I_{OH} = -24\text{mA}$	3.3	Full	2.5			V
		4.5V		3.8			
	$I_{OH} = -32\text{mA}$	5V		4.2			
		5.5V		4.8			
V_{OL}	$I_{OL} = 100\mu\text{A}$	2.0V to 5.5V				0.1	
	$I_{OH} = 8\text{mA}$	2.0				0.45	
	$I_{OH} = 24\text{mA}$	3.3	Full			0.55	V
		4.5V				0.55	
	$I_{OL} = 32\text{mA}$	5V				0.5	
		5.5V				0.45	
I_I	A or B inputs $V_I=5.5\text{V or GND}$	0V to 5.5V	+25 $^\circ\text{C}$ Full		± 0.1	± 5	μA
I_{off}	$V_I \text{ or } V_O=5.5\text{V}$	0V	+25 $^\circ\text{C}$ Full		± 0.1	± 10	μA
I_{CC}	$V_I=5.5\text{V or GND}, I_O=0$	2.0V to 5.5V	+25 $^\circ\text{C}$ Full		0.1	10	μA
I_{CCT}	One input at 3.4V, Other inputs at V_{CC} or GND	5.5V	Full			500	μA
C_i (Input Capacitance)	$V_{CC}=0\text{V}, f=10\text{MHz}$	0V	+25 $^\circ\text{C}$		6		pF

(1) All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

(2) Limits are 100% production tested at 25 $^\circ\text{C}$. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

AC Characteristics

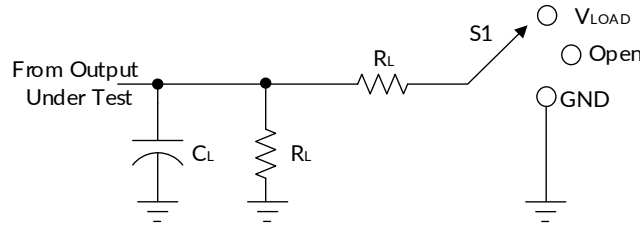
PARAMETER	SYMBOL	TEST CONDITIONS		MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT
Propagation Delay	t_{pd}	$V_{CC}=2.0V\pm0.2V$	$C_L=30pF, R_L=500\Omega$		3.8		
		$V_{CC}=3.3V\pm0.3V$	$C_L=50pF, R_L=500\Omega$		2.6		ns
		$V_{CC}=5V\pm0.5V$	$C_L=50pF, R_L=500\Omega$		2.8		
Power dissipation capacitance	C_{pd}	$V_{CC}=5V$	$f=10MHz$		25		pF

(1) All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

(2) This parameter is ensured by design and/or characterization and is not tested in production.

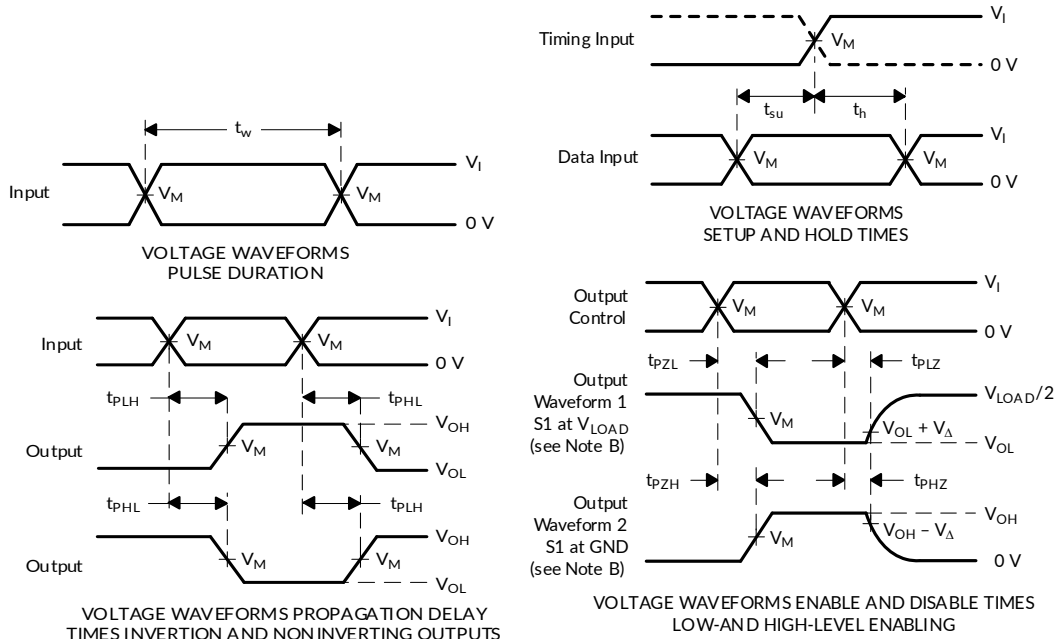
(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

PARAMETER MEASUREMENT INFORMATION



TEST			S1				
t_{PLH}/t_{PHL}			Open				
t_{PLZ}/t_{PZL}			V_{LOAD}				
t_{PHZ}/t_{PZH}			GND				

INPUTS							
V_{CC}	V_I	t_r/t_f	V_M	V_{LOAD}	C_L	R_L	V_{Δ}
$2.0V \pm 0.2V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	30pF	500 Ω	0.15V
$3.3V \pm 0.3V$	3V	$\leq 2.5ns$	1.5V	6V	50pF	500 Ω	0.3V
$5V \pm 0.5V$	V_{CC}	$\leq 2.5ns$	$V_{CC}/2$	$2 \times V_{CC}$	50pF	500 Ω	0.3V



NOTES: A. C_L includes probe and jig capacitance.

B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics: $PRR \leq 10 \text{ MHz}$, $Z_O = 50\Omega$.

D. The outputs are measured one at a time, with one transition per measurement.

E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .

F. t_{PZL} and t_{PZH} are the same as t_{en} .

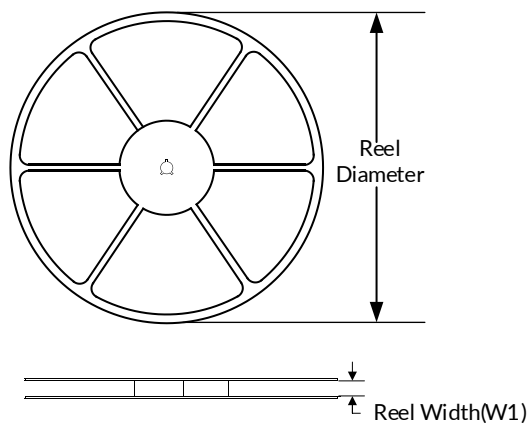
G. t_{PLH} and t_{PHL} are the same as t_{pd} .

H. All parameters and waveforms are not applicable to all devices.

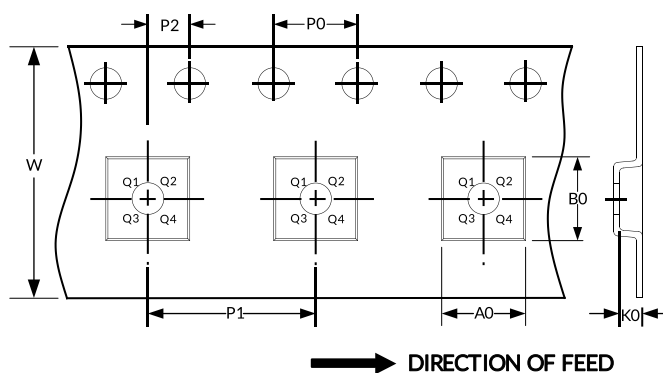
Figure 1. Load Circuit and Voltage Waveforms

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3

NOTE:

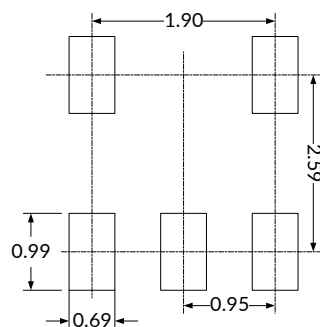
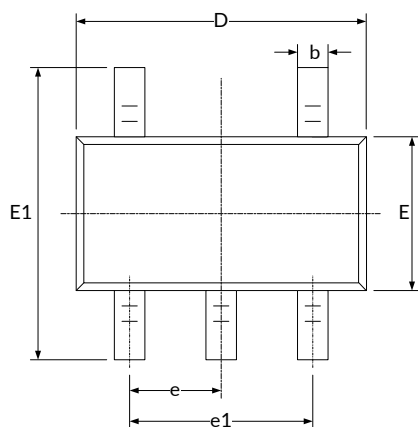
1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

PACKAGE OUTLINE DIMENSIONS

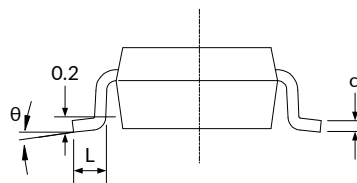
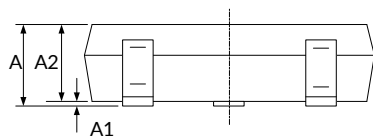
NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

SOT23-5 ⁽³⁾



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D ⁽¹⁾	2.820	3.020	0.111	0.119
E ⁽¹⁾	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC) ⁽²⁾		0.037(BSC) ⁽²⁾	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°